

NCE01P13K

NCE P-Channel Enhancement Mode Power MOSFET

Description

The NCE01P13K uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications. It is ESD protested.

General Features

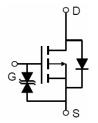
- V_{DS} =-100V, I_{D} =-13A $R_{DS(ON)}$ <200mΩ @ V_{GS} =-10V (Typ:170mΩ)
- Super high dense cell design
- Advanced trench process technology
- Reliable and rugged
- High density celldesign for ultra low on-resistance

Application

- Power switch
- DC/DC converters

100% UIS TESTED!

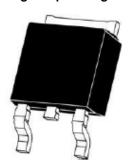
100% AVds TESTED!



Schematic diagram



Marking and pin assignment



TO-252 -2Ltop view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE01P13K	NCE01P13K	TO-252-2L	-	-	-

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	-100	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	-13	А
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	-9.2	Α
Pulsed Drain Current	I _{DM}	-30	Α
Maximum Power Dissipation	P _D	40	W
Derating factor		0.32	W/°C
Single pulse avalanche energy (Note 5)	E _{AS}	110	mJ
Operating Junction and Storage Temperature Range	T_J, T_STG	-55 To 150	$^{\circ}\mathbb{C}$



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Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{ heta Jc}$	3.13	°C/W	
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Electrical Characteristics (T_c=25 ℃ unless otherwise noted)

Parameter Symbol		Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-100	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-100V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±10	μA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1	-1.9	-3	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-16A	-	170	200	mΩ
Forward Transconductance	g FS	V _{DS} =-15V,I _D =-5A	12	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ - 25\/\/ -0\/	-	760	-	PF
Output Capacitance	Coss	V_{DS} =-25V, V_{GS} =0V, F=1.0MHz	-	260	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVID2	-	170	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t _{d(on)}		-	14	-	nS
Turn-on Rise Time	t _r	V _{DD} =-50V,I _D =-10A	-	18	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{GEN} =9.1 Ω	-	50	-	nS
Turn-Off Fall Time	t _f		-	18	-	nS
Total Gate Charge	Q_g	V = 50V I = 10A	-	25	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =-50V, I_{D} =-10A, V_{GS} =-10V	_	5	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =-10V	-	7	-	nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-10A	-	-	-1.2	V
Diode Forward Current (Note 2)	Is	-	-	-	-13	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF =-10A	-	35	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	46	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD				y LS+LD)

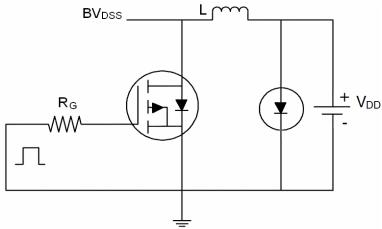
Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- **5.** E_{AS} condition: Tj=25 $^{\circ}$ C,V_{DD}=-50V,V_G=-10V,L=0.5mH,Rg=25 Ω

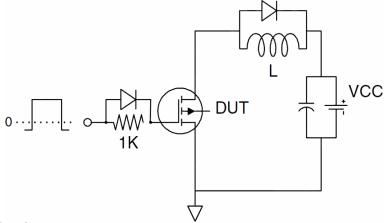
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Test Circuit

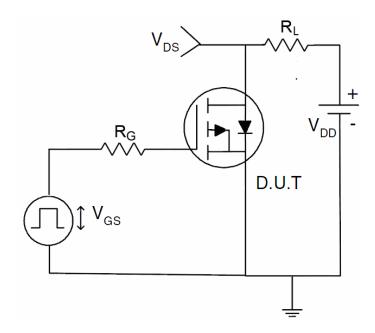
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)

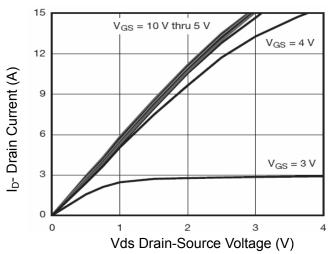


Figure 1 Output Characteristics

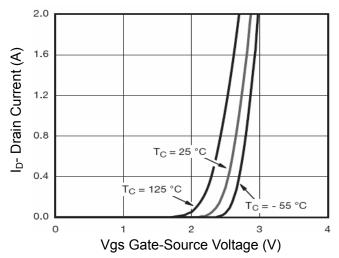


Figure 2 Transfer Characteristics

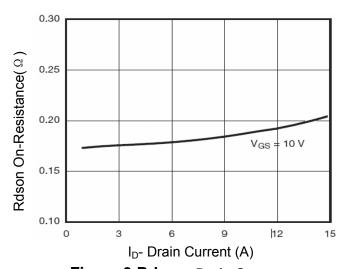


Figure 3 Rdson- Drain Current

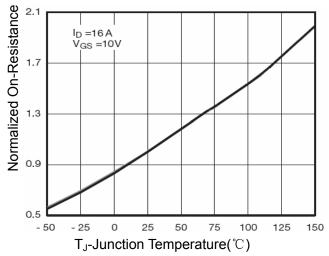


Figure 4 Rdson-JunctionTemperature

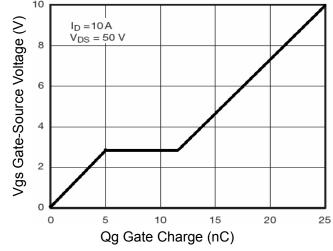


Figure 5 Gate Charge

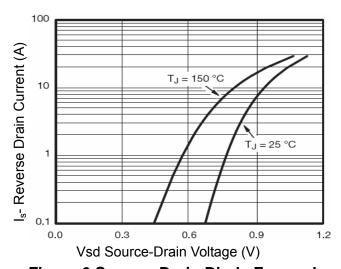
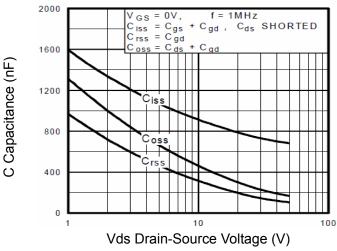


Figure 6 Source- Drain Diode Forward



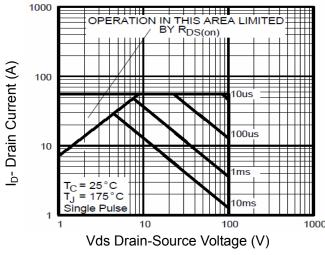


(V) 112 8 8 4 9 100 125 150 175 T_C Case Temperature (°C)

20

Figure 7 Capacitance vs Vds

Figure 9 Drain Current vs Case Temperature



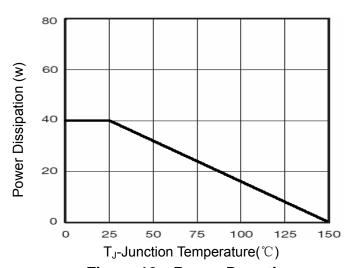


Figure 8 Safe Operation Area

Figure 10 Power De-rating

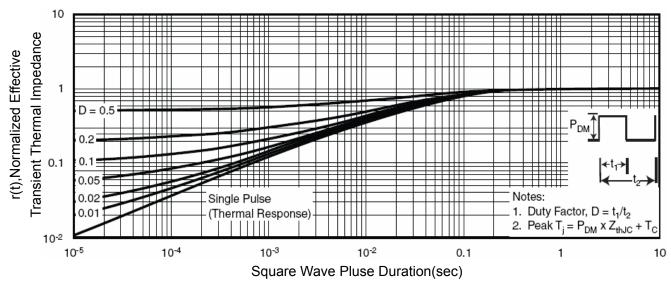
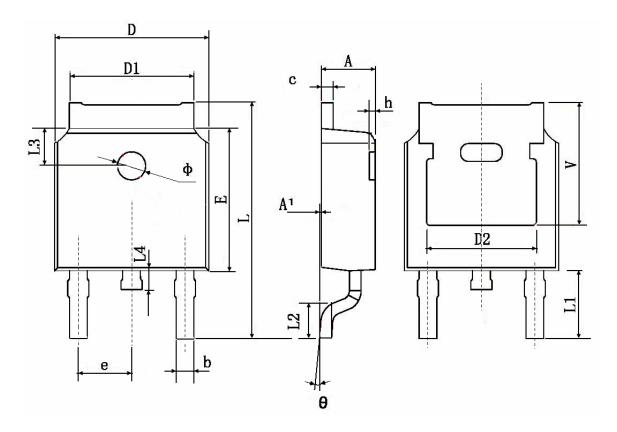


Figure 11 Normalized Maximum Transient Thermal Impedance

Pb Free Product

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TO-252 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	4.830 TYP.		TYP.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	TYP.	P. 0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3	1.600	1.600 TYP.		TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	TYP.	0.211 TYP.		



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