

COVER PAGE

Page List

Page	Content
1	Connector
2	DDR3-256Mx16x2
3	NAND FLASH
4	POWER MANAGEMENT
5	USB PORTS & UART
6	ZYNQ CONFIG (BANK0)
7	ZYNQ DDR (BANK 502)
8	ZYNQ PWR & GND
9	ZYNQ PSIO (BANK500 & 501)
10	ZYNQ PLIO (BANK 34 & 35)

参考电路:

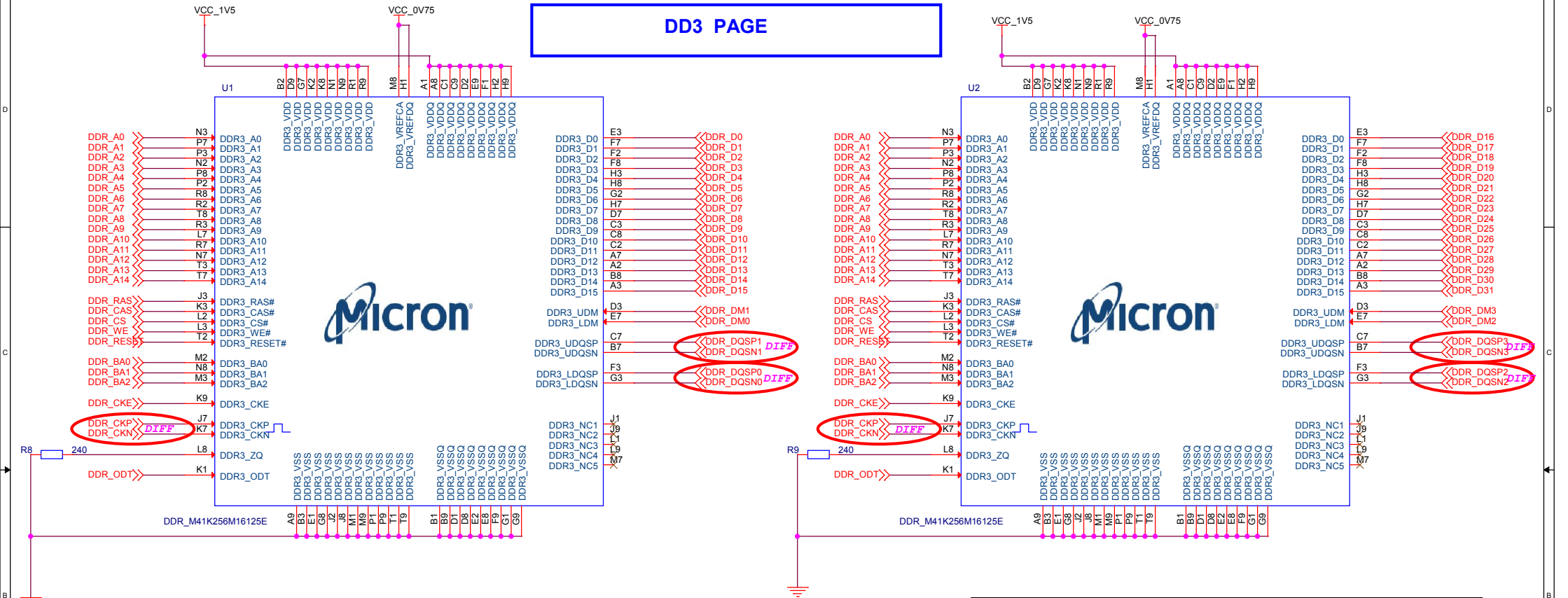
MicroZed

Zedboard

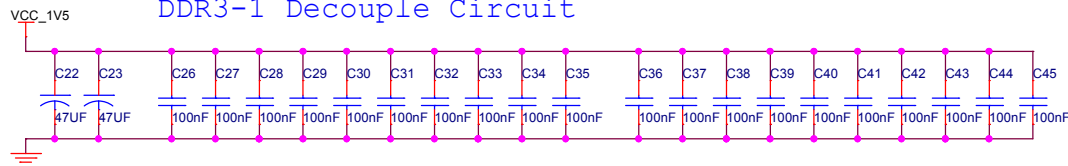
Parallella

Title		
<Title>		
Size A	Document Number <Doc>	Rev <RevCode>
Date:	Sunday, July 13, 2014	Sheet 1 of 11

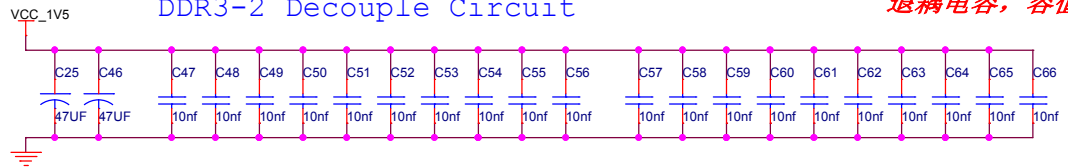
DD3 PAGE



DDR3-1 Decouple Circuit



DDR3-2 Decouple Circuit

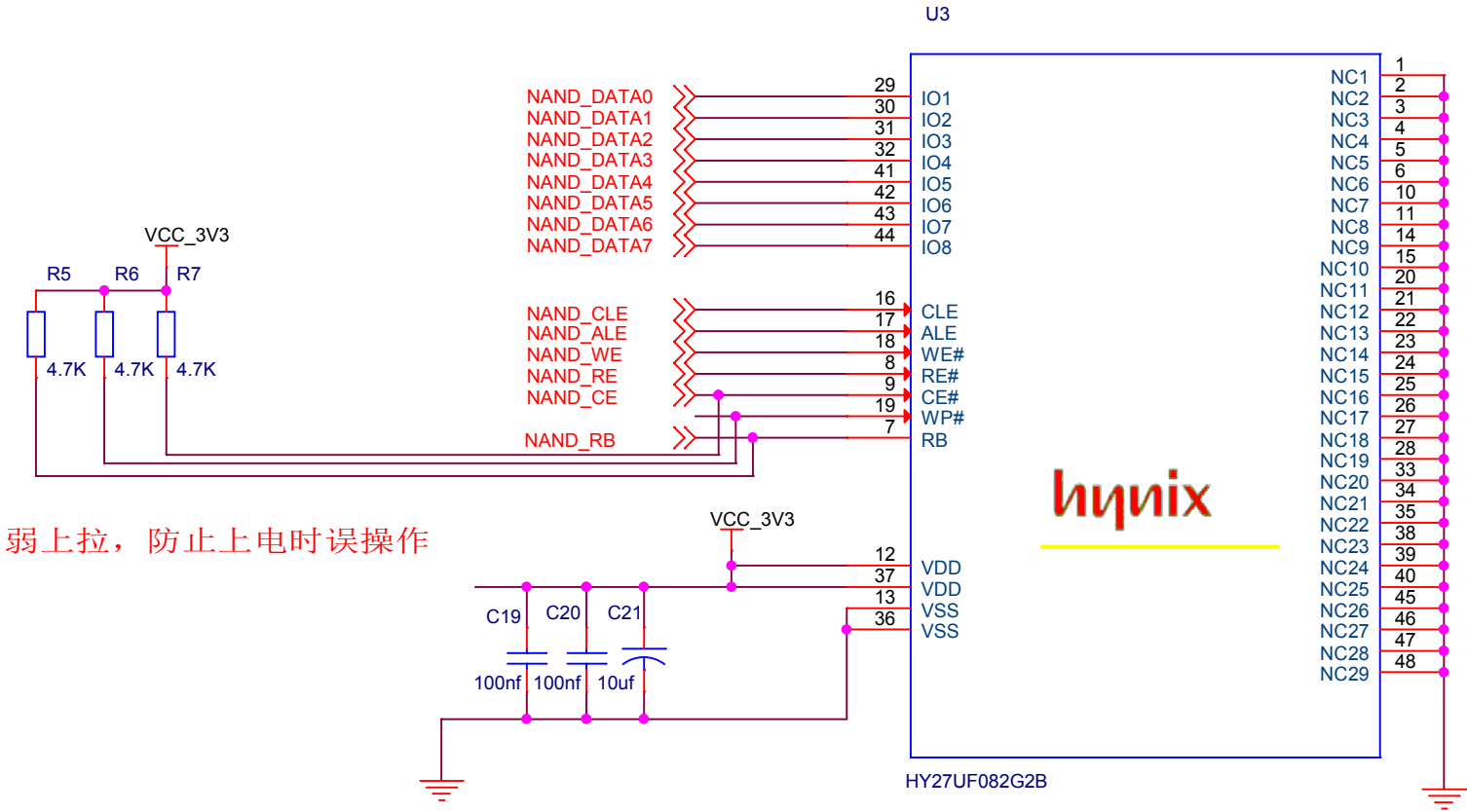


退耦电容，容值越小，距离引脚越近

Common Address and Control Signals
Independent Data Signals

Title			<Title>
Size	Document Number		Rev
B	<Doc>		<RevCode>
Date:	Sunday, July 13, 2014		Sheet 3 of 11

NandFlash Page



Title			
<Title>			
Size	Document Number		Rev
A	<Doc>		<RevCode>
Date:	Sunday, July 13, 2014		Sheet 4 of 11

Power PAGE

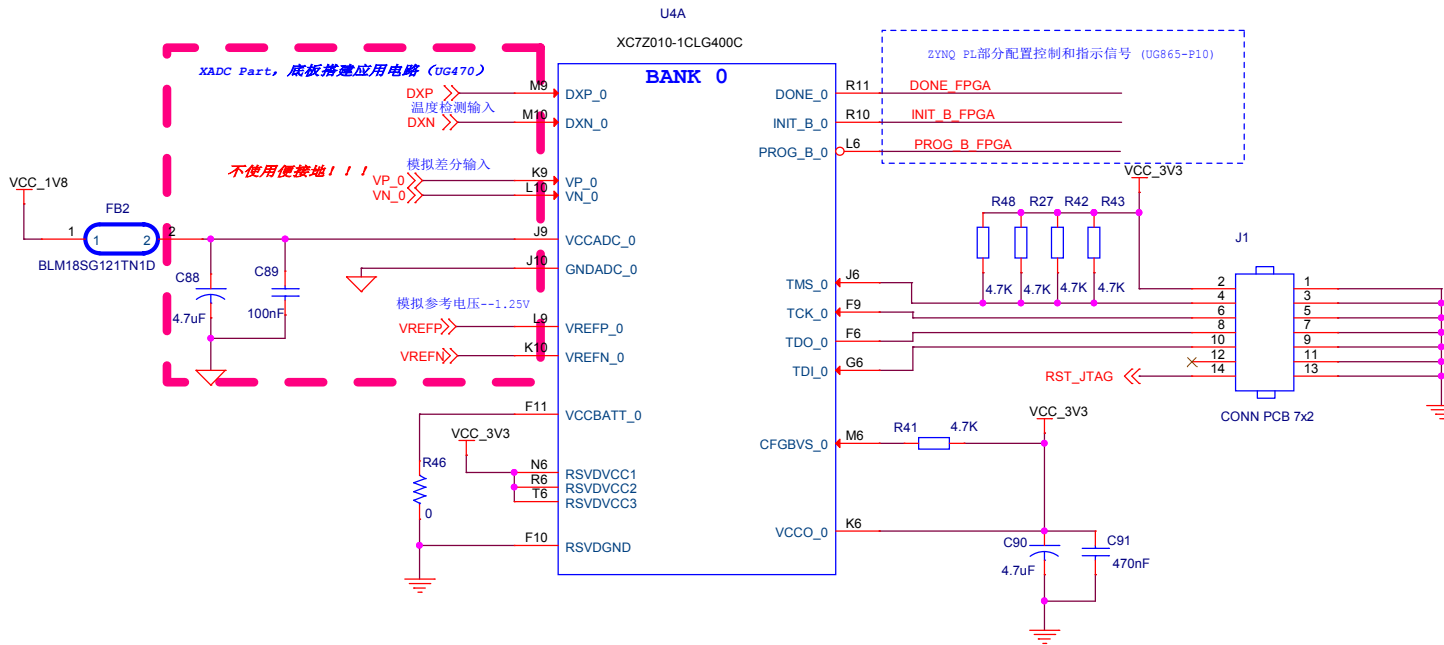
Title				
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Size B	Document Number			Rev
	<Doc>			<RevCode>
Date:	Sunday, July 13, 2014	Sheet	5 of 11	

USB OTG & USB UART

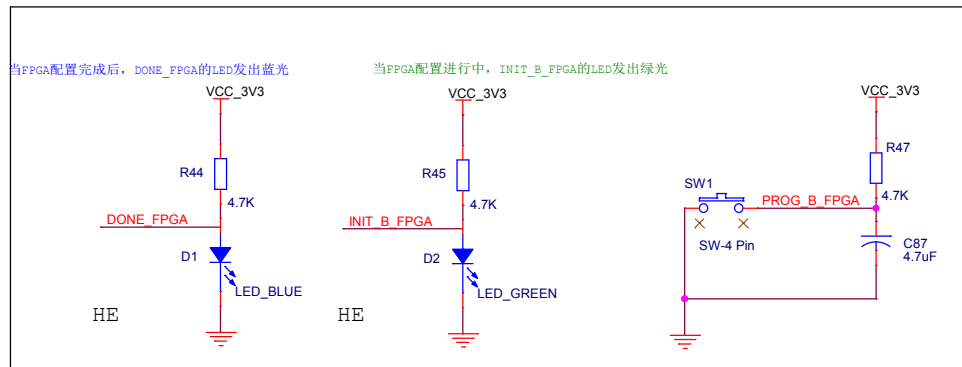


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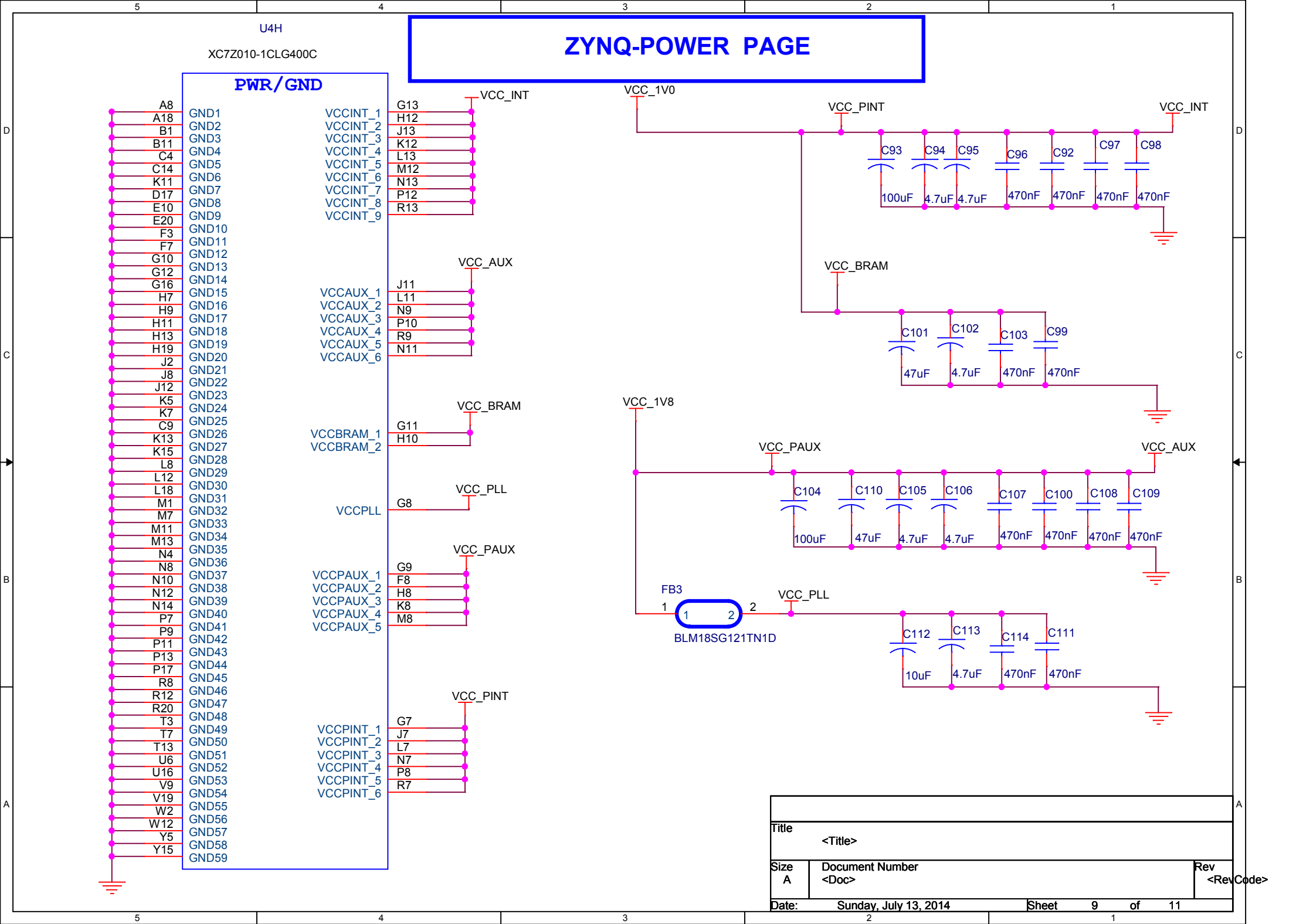
ZYNQ CONFIGURE PAGE



Reserved for Xc7z020-clg400



Title		<Title>
Size	Document Number	Rev
B	<Doc>	<RevCode>
Date:	Sunday, July 13, 2014	Sheet 7 of 11

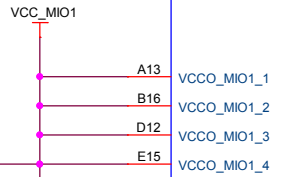
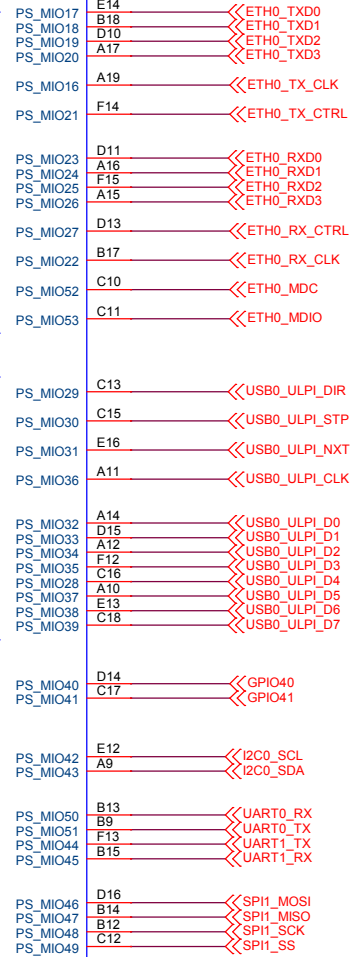


ZYNQ-PSIO PAGE

U4C
XC7Z010-1CLG400C

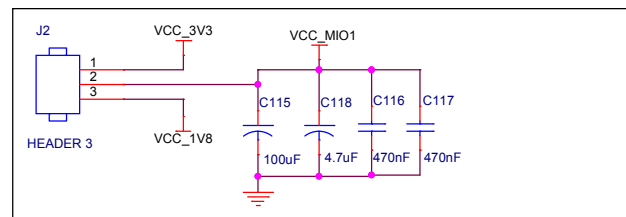
BANK 501

B10 PS_SRST_B



当使用RGMII时, 为MIO1的一半, 否则浮空 (ug865-p12)

BANK501 采用的Io电平为可选电平, 1.8v或者3.3v!!!

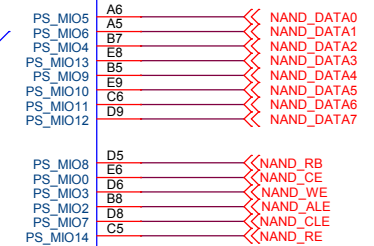
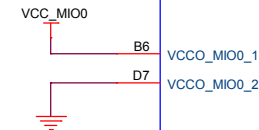


U4B
XC7Z010-1CLG400C

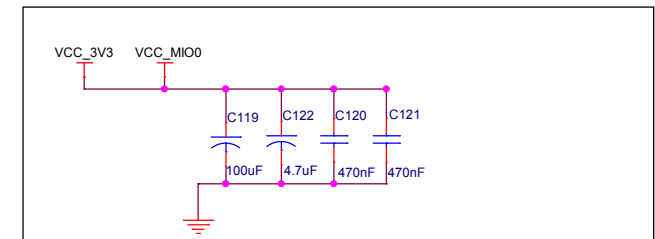
BANK 500

E7 PS_CLK

C7 PS_POR_B



BANK500 采用3.3v的Io电平, GPIO0和GPIO15工作电平为3.3v



Title			<Title>
Size	Document Number	Rev	
B	<Doc>	<RevCode>	
Date:	Sunday, July 13, 2014	Sheet	10 of 11

ZYNQ-PLIO PAGE

U4F		
XC7Z010-1CLG400C		
BANK 34		
R19	IO_0_34	IO_25_34 T19
T11	IO_L1P_T0_34	IO_L13P_T2_MRCC_34 N18
T10	IO_L1N_T0_34	IO_L13N_T2_MRCC_34 P19
T12	IO_L2P_T0_34	IO_L14P_T2_SRCC_34 N20
U12	IO_L2N_T0_34	IO_L14N_T2_SRCC_34 P20
U13	IO_L3P_T0_DQS_PUDC_B_34	IO_L15P_T2_DQS_34 T20
V13	IO_L3N_T0_DQS_34	IO_L15N_T2_DQS_34 U20
V12	IO_L4P_T0_34	IO_L16P_T2_34 V20
W13	IO_L4N_T0_34	IO_L16N_T2_34 W20
T14	IO_L5P_T0_34	IO_L17P_T2_34 Y18
T15	IO_L5N_T0_34	IO_L17N_T2_34 Y19
P14	IO_L6P_T0_34	IO_L18P_T2_34 V16
R14	IO_L6N_T0_VREF_34	IO_L18N_T2_34 W16
Y16	IO_L7P_T1_34	IO_L19P_T3_34 R16
Y17	IO_L7N_T1_34	IO_L19N_T3_VREF_34 R17
W14	IO_L8P_T1_34	IO_L20P_T3_34 T17
Y14	IO_L8N_T1_34	IO_L20N_T3_34 R18
T16	IO_L9P_T1_DQS_34	IO_L21P_T3_DQS_34 V17
U17	IO_L9N_T1_DQS_34	IO_L21N_T3_DQS_34 V18
V15	IO_L10P_T1_34	IO_L22P_T3_34 W18
W15	IO_L10N_T1_34	IO_L22N_T3_34 W19
U14	IO_L11P_T1_SRCC_34	IO_L23P_T3_34 N17
U15	IO_L11N_T1_SRCC_34	IO_L23N_T3_34 P18
U18	IO_L12P_T1_MRCC_34	IO_L24P_T3_34 P15
U19	IO_L12N_T1_MRCC_34	IO_L24N_T3_34 P16
N19	VCCO_34_1	
R15	VCCO_34_2	
T18	VCCO_34_3	
V14	VCCO_34_4	
W17	VCCO_34_5	
Y20	VCCO_34_6	

U4G		
XC7Z010-1CLG400C		
BANK 35		
G14	IO_0_35	IO_25_34 J15
C20	IO_L1P_T0_AD0P_35	IO_L13P_T2_MRCC_35 H16
B20	IO_L1N_T0_AD0N_35	IO_L13N_T2_MRCC_35 H17
B19	IO_L2P_T0_AD8P_35	IO_L14P_T2_AD4P_SRCC_35 J18
A20	IO_L2N_T0_AD8N_35	IO_L14N_T2_AD4N_SRCC_35 H18
E17	IO_L3P_T0_DQS_AD1P_35	IO_L15P_T2_DQS_AD12P_35 F19
D18	IO_L3N_T0_DQS_AD1N_35	IO_L15N_T2_DQS_AD12N_35 F20
D19	IO_L4P_T0_35	IO_L16P_T2_35 G17
D20	IO_L4N_T0_35	IO_L16N_T2_35 G18
E18	IO_L5P_T0_AD9P_35	IO_L17P_T2_AD5P_35 J20
E19	IO_L5N_T0_AD9N_35	IO_L17N_T2_AD5N_35 H20
F16	IO_L6P_T0_35	IO_L18P_T2_AD13P_35 G19
F17	IO_L6N_T0_VREF_35	IO_L18N_T2_AD13N_35 G20
M19	IO_L7P_T1_AD2P_35	IO_L19P_T3_35 H15
M20	IO_L7N_T1_AD2N_35	IO_L19N_T3_VREF_35 G15
M17	IO_L8P_T1_AD10P_35	IO_L20P_T3_AD6P_35 K14
M18	IO_L8N_T1_AD10N_35	IO_L20N_T3_AD6N_35 J14
L19	IO_L9P_T1_DQS_AD3P_35	IO_L21P_T3_DQS_AD14P_35 N15
L20	IO_L9N_T1_DQS_AD3N_35	IO_L21N_T3_DQS_AD14N_35 N16
K19	IO_L10P_T1_AD11P_35	IO_L22P_T3_AD7P_35 L14
J19	IO_L10N_T1_AD11N_35	IO_L22N_T3_AD7N_35 L15
L16	IO_L11P_T1_SRCC_35	IO_L23P_T3_35 M14
L17	IO_L11N_T1_SRCC_35	IO_L23N_T3_35 M15
K17	IO_L12P_T1_MRCC_35	IO_L24P_T3_AD15P_35 K16
K18	IO_L12N_T1_MRCC_35	IO_L24N_T3_AD15N_35 J16
C19	VCCO_35_1	
F18	VCCO_35_2	
H14	VCCO_35_3	
J17	VCCO_35_4	
K20	VCCO_35_5	
M16	VCCO_35_6	

Title		
<Title>		
Size	Document Number	Rev
B	<Doc>	<RevCode>
Date:	Sunday, July 13, 2014	Sheet 11 of 11